

AMENDMENTS TO THE SPECIFICATION:

Please replace the paragraph beginning at page 4, line 4, with the following rewritten paragraph:

-- Figure 1 shows a schematic layout of a test system based on an antigen/antibody reaction at the gold electrodes of a sensor chip, or biochip,--

Please replace the paragraph beginning at page 4, line 21, with the following rewritten paragraph:

-- - The silicon chips which are contacted via a circuit board with the readout device carry a different number of interdigital electrode structures 12 depending on layout, which are arranged in pairs, that is always node/cathode, and depending on chip layout have a variable diameter, (with the same diameter always being used on one chip). The electrodes (13) are [[the]] comb-like electrode fingers of the interdigital structures always have a width of 1 μm in such cases and a spacing between the electrode pairs of a maximum of 1.0 μm , in addition there is a electrode made of gold on the chip. The Ag/AgCl reference electrode 9, against which the electrode potential is set, is not located on the chip, it is integrated into the flow system and can however optionally also be realized on the chip.--

Please replace the paragraph beginning at page 5, line 7, with the following rewritten paragraph:

-- -The antibody 5 is immobilized on the gold electrodes of the sensor chip, or biochip. This occurs, depending on the type of the antibody 5, with the aid of a protein layer made of protein A, G or G'.--

Please replace the paragraph beginning at page 5, line 23, with the following rewritten paragraph:

--The antigen verification is to be realized by the following layer structure on the gold Interdigital structures of the sensor chip, or biochip, **Fig. 1**.--

Please replace the paragraph beginning at page 6, line 16, with the following rewritten paragraph:

--The schematic layout of the system for operation of a biosensor based on an antigen/antibody reaction, which can be read out accordingly at the gold electrodes 3 of the sensor chip 10, or biochip of **Fig. 1**.--